

9 Virtual Memory

- All the memory management policies try to keep a number of processes in the memory at the same time to allow multiprogramming. But they require process to be loaded in the memory before execution.
- With the virtual memory technique, we can execute a process which is only partially loaded in memory. Thus, the logical address space may be larger than physical memory, and we can execute more processes in memory at a time.

9 Virtual Memory

By means of virtual memory,

- Only part of the program needs to be in memory for execution
- Logical address space can therefore be much larger than physical address space
- A greater degree of multiprogramming can be possible
- OS allows more efficient process creation
- Virtual memory can be implemented by:
 - Demand paging
 - Demand segmentation



9.1 Demand Paging

- Demand paging is the most common virtual memory management system. In demand paging, programs reside on a swapping device commonly known as the backing store. The backing store, for most of today's operating systems is a disk.
- When the operating system decides to start a new process, it swaps only a small part of this new process into memory.

9.1 Demand Paging

- The page table of this new process is prepared and loaded into memory. In the page table, there is a column to show valid/invalid bits which show whether that page in memory (valid) or not (invalid).
- If the executing process tries to access a page which is not in the memory (invalid), a page fault occurs.





9.3 Page Faults When a page fault happened, the OS finds the desired page on the disk and looks for a free frame on the main memory. If there is no free frame, the OS must choose a frame to swap it out to the disk. Then, the valid/invalid bit of the chosen page is changed as "i". Now the desired page is swapped into newly freed frame, its frame table is modified, and the valid/invalid bit of it is set to valid.



- Check whether the page reference is valid, when a page fault occurred.
- If yes, determine the location of the required page on the backing store.
- Find a free frame and swap in the required page from the backing store into the free frame.
- When I/O is completed, restore registers and process state for the process which caused the page fault and save state of the currently executing process.
- Modify the corresponding page table entry to show that the recently copied page is now in memory.
- Restart instruction that caused the page fault.



9.3 Page Faults These operations can be summarized as:

- (FAST) Checking the address and choosing a frame
- (SLOW) Swap out the chosen frame to secondary storage (mostly disk is used)
- (SLOW) Swap in the page from secondary storage into the vacated frame.
- (FAST) Restart the process

In servicing a page fault, the time is spent mainly for swap-out and swap-in. The periods of other operations can be negligible.

9.4 Performance

- Page Fault Probability $0 \le p \le 1.0$
 - if p = 0, it means no page faults
 if p = 1, every reference is a fault
- Effective Access Time (eat) = $(1 p)^* \text{eat}_{no-pf} + p^* \text{eat}_{pf}$
- eatno-pf = memory access time
- eatpf = page fault overhead + swap page out + swap page in+ restart overhead

In briefly, eat_{pf} can be represented by "page fault service time".

9.4 Performance

Example:

- Memory access time = 200 ns
- Average page fault service time = 8 ms
 - $eat = (1 p) \times 200 \text{ ns} + p \times 8 \text{ ms}$
 - = 200 (1 p) + 8,000,000 p
 - = 200 + 7,999,800 p ns
- If one access out of 1000 (p=0.001) causes a page fault, then
 eat = 8.2 ms

According to eatno-pf, there is a slowdown by a factor of 40K.

9.5 Page Replacement

- A page replacement algorithm determines how the page to be replaced is selected when a page fault occurs. The aim is to minimize the page fault rate.
- The efficiency of a page replacement algorithm is evaluated by running it on a particular string of memory references and computing the number of page faults. Reference strings are either generated randomly, or by tracing the paging behavior of a system and recording the page number for each logical memory reference.







9.5.1 Dirty Bit

- □ The dirty bit allows performance optimization.
- This strategy requires that the backing store retain a copy of the page after it was swapped into memory.
- The dirty bit is set to "0" by the hardware when the page is swapped in.
- When we select a frame by using a page replacement algorithm, we examine its dirty bit.

9.5.1 Dirty Bit

- If it is "1", that means the page has been modified since it was swapped in.
- In this case we have to copy that page into the backing store.
- However if the dirty bit is "0", that means the page has not been modified since it was swapped in, so the copy in the backing store is valid.

9.5.2 Optimal Page Replacement

- Optimal Page Replacement (OPT) chooses the page which will not be used for the longest period.
- For a fixed number of frames, OPT has the lowest page fault rate among the page replacement algorithms.
- But OPT is not possible to be implemented in practice.
 Because it requires future knowledge. However, it is used for performance comparison.

9.5.2 Optimal Page Replacement

Example

- Assume we have 3 frames and consider the reference string of 5, 7, 6, 0, 7, 1, 7, 2, 0, 1, 7, 1, 0
- Show the content of memory after each memory reference if OPT page replacement algorithm is used.
 Find also the number of page faults.







9.5.3 First-In-First-Out (FIFO)

As an exercise consider the reference string below, and find the number of page faults applying the FIFO method with 3 and 4 paged main memory. Then, examine whether the replacement suffer Belady's anomaly.

1, 2, 3, 4, 1, 2, 5, 1, 2, 3, 4, 5

9.5.4 Least Recently Used (LRU)

In this algorithm, the chosen is the page that has not been used for the longest period.

The OS sets the reference bit of a page to "1" when it is referenced. This bit will not give the order of use but it will simply tell whether the corresponding frame is referenced recently or not. The OS resets all reference bits periodically.





In order to be able to decide on the page replacement scheme of a particular reference string, we have to know the number of page frames available. In page replacement, some frame allocation policies may be followed.

- Global Replacement: A process can replace any page in the memory.
- Local Replacement: Each process can replace only from its own reserved set of allocated page frames.

9.6 Frame Allocation

In case of local replacement, the operating system should determine how many frames should the OS allocate to each process. The number of frames for each process may be adjusted by using two ways:

Equal Allocation

Proportional Allocation

9.6 Frame Allocation Equal allocation: If there are p processes and n frames, frame allocation for process p is as below: f(p) = n / p Proportional allocation: If v(p) is virtual memory size of process p, there are m processes and n frames, then the total virtual memory size will be: V = Σv(p)

$$V = \Sigma v(p)$$

Frame allocation for process p is as below:

f(p) = n * v(p) / V

9.6 Frame Allocation

Example: Consider a system having 64 frames and there are 4 processes with the following virtual memory sizes: v(1) = 16, v(2) = 128, v(3) = 64 and v(4) = 48.

Equal Allocation

Assume that there are n frames, and p processes, then n/p frames are allocated to each process allocates 64 / 4 = 16 frames to each process.

9.6 Frame Allocation

Example: Consider a system having 64 frames and there are 4 processes with the following virtual memory sizes: v(1) = 16, v(2) = 128, v(3) = 64 and v(4) = 48.

Proportional Allocation

V = 16 + 128 + 64 + 48 = 256. It allocates: (16 / 256) * 64 = 4 frames to process 1, (128/256) * 64 = 32 frames to process 2, (64 / 256) * 64 = 16 frames to process 3, (48 / 256) * 64 = 12 frames to process 4.

9.7 Thrashing

Because of frequent page faults, if a process is spending more time for paging in/out than executing, then it is called as thrashing. Thrashing causes considerable decreasing in system performance. If a process does not have enough number of frames, it will send a page fault. Although a page replacement is done, if all pages are in active use, another page selection to replace will be needed in a very short time. This means another page fault will be issued shortly, and so on.

93 Social replacement algorithms can limit the effects of thrashing. If the degree of multiprogramming is increased over a limit, processor utilization falls down considerably because of thrashing. Processor Utilization Output: Degree of multiprogramming

9.8 Working Set

To prevent thrashing, the process must be supported by providing as many frames as it needs. For this, a model called the working set model is developed. The working set of a process is the set of pages of the process that are currently resident in physical memory.

The working set contains only pageable memory allocations; nonpageable memory allocations or large page allocations are not included in the working set.

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9.8 Working Set

T parameter is chosen so that all pages referenced in the last T seconds comprise the working set.

- If T is chosen too small, it won't cover entire working set.
- If T is too large, several localities of a process may overlap.

Then, compute the WS size (WSS) for each process, and find the total demand (D) of the system at that time instance, as the summation of all the WS sizes.

 $D(t_{now}) = \Sigma WSS_i(t_{now})$ for i=1,...,p

9.8 Working Set

If the number of frames is n, then

- $_{\alpha}$. If D > n , the system is thrashing.
- b. If $\mathsf{D} < \mathsf{n}$, the system is all right, the degree of
 - multiprogramming can possibly be increased.

If D > n at any time instant, OS selects a process to suspend for a while. The frames that were used by the selected process are reallocated to other processes.



